



**Exam Code:** e20-340

**Exam Name:** clariion solutiongs implementation

**Vendor:** EMC

**Version:** DEMO

## Part: A

1: Which CLARiiON option can minimize the write performance impact of certain hardware failure conditions?

- A. Checking the HA Vault option prevents write cache from being disabled if a vault disk fails
- B. Checking the HA Vault option prevents write cache from being disabled if an SP fails
- C. Unchecking the HA Vault option prevents write cache from being disabled if an SP fails
- D. Unchecking the HA Vault option prevents write cache from being disabled if a vault disk fails

**Correct Answers: D**

2: What is the M Series fabric security implementation?

- A. PROtegrity
- B. Invista
- C. SANtegrity
- D. Secure Fabric OS

**Correct Answers: C**

3: Your customer has two CLARiiON storage systems. A new host is installed running a volume manager.

How should you configure the CLARiiONs for load balancing and storage processor distribution?

- A. Create cross-plaids on the same array
- B. Create multisystem plaids
- C. Create plaids using different (odd/even) buses
- D. Create plaids avoiding DAE crossing

**Correct Answers: B**

4: Which command will return information for SAN Copy sessions running on SPA?

- A. java -jar navicli.jar -h sancopy -info
- B. java -jar navicli.jar -h sancopy -getinfo
- C. navicli -h sancopy -collect
- D. naviseccli -h sancopy -info

**Correct Answers: D**

5: Which CLARiiON feature allows the replacement of a disk drive without operating in degraded mode during the hot spare rebuild operation?

- A. Fast Hot Spare
- B. Hot Spare
- C. Proactive Hot Spare
- D. Protective Hot Spare

**Correct Answers: C**

6: A CX3-40 has just been added to the SAN, but storage processor ports do not appear in the SAN.

What is the probable cause?

- A.Global Security is not initialized on the array.
- B.SP port speeds do not match the switch port speeds.
- C.The array may be set to Arbitrated Loop mode.
- D.The array has Access Logix enabled.

**Correct Answers: B**

7: The IPS 4 and 8 Storage Services Modules allow which protocols to function simultaneously on any port?

- A.FC and IPX
- B.FCIP and iSCSI
- C.IP and FCIP
- D.iSCSI and FC

**Correct Answers: B**

8: Which M Series switch supports arbitrated loop?

- A.ED-140M
- B.ED-24000M
- C.DS-24M
- D.DS-8M

**Correct Answers: C**

9: Click the Exhibit button.

The drive labeled "B" would be in which drive slot?



- A.10
- B.11
- C.9
- D.8

**Correct Answers: B**

10: What is NOT true about the Emulex Utility (lputil)?

- A.It can be used even if HBAnywhere is not installed.
- B.It can be used to do firmware upgrades.
- C.It can be used to look at bindings, status and WWNs.
- D.It can be used to view or change the PCI configuration parameters.

**Correct Answers: A**

11: What is the primary resource for supported attach topologies?

- A. CLARiiON Configuration Planning Form
- B. CLARiiON Procedure Generator
- C. EMC Solutions Guide
- D. Open Systems Configuration Guide

**Correct Answers: D**

12: What is fan-out?

- A. Maximum number of initiators that can access a single storage port through a SAN
- B. Maximum number of storage ports that can be accessed by a single initiator through a SAN
- C. Minimum number of storage ports that can be accessed by a single initiator through a SAN
- D. Minimum number of initiators that can access a single storage port through a SAN

**Correct Answers: A**

13: What security solution exists for B Series SANs?

- A. Host Fabric DS
- B. Host Fabric OS
- C. Secure Fabric DS
- D. Secure Fabric OS

**Correct Answers: D**

14: The customer wants to use Incremental SAN Copy to transfer data. The source for the transfer is a 512 GB clone LUN. At what granularity will the changes on the source LUN be tracked?

- A. 128 KB
- B. 64 KB
- C. 2 MB
- D. 1 MB

**Correct Answers: C**

15: When must the DAE2 enclosure address be set?

- A. After powering up and connecting all back-end cables
- B. After powering up the enclosure but before connecting to the back-end loop
- C. Before powering up the enclosure
- D. It can be set at any time as long as no LUNs are bound

**Correct Answers: C**